

Claims:

1. A processor resetting apparatus comprising:
a fibre channel arbitrated loop (FC-AL) interface
5 arranged to receive a frame containing an indicator of
a reset command for a server including a processor
associated with said resetting apparatus; and
reset means, responsive to said reset command, to
issue a reset command for resetting said processor.
- 10 2. Apparatus as claimed in claim 1 wherein the server
is one of a redundant pair of servers.
3. Apparatus as claimed in claim 1 wherein the
15 apparatus comprises one of a separate component of a
server motherboard or an integral element of a server
motherboard.
4. Apparatus as claimed in claim 1 wherein said FC-AL
20 interface is arranged to receive a frame indicative of
a lock request for a resource and wherein said
apparatus further comprises:
means for receiving from said associated processor
an indicator of a resource to be locked;
25 means for causing a corresponding indicator to be
stored;
means for causing said stored indicator to be
deleted when an associated resource is unlocked;
means, responsive to receiving a lock request
30 frame originating from another processor, for checking
any stored indicators for a matching locked resource;
means, responsive to detecting a match, for
transmitting a frame indicative of said resource being

locked by said processor to the originator of said lock request; and

means, responsive to not detecting a match, for transmitting said lock request frame to the originator
5 of said lock request.

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